

$$a+1=1$$

$$S = 2^m \times n$$

Riepilogo 30/9/19

representazione binaria (naturale)
 esperimento
 algebra booleana: elem. forzante/neutro
 logica positiva/negativa

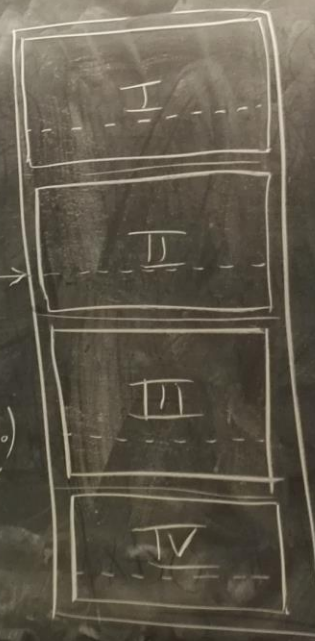
chip di memoria / banco

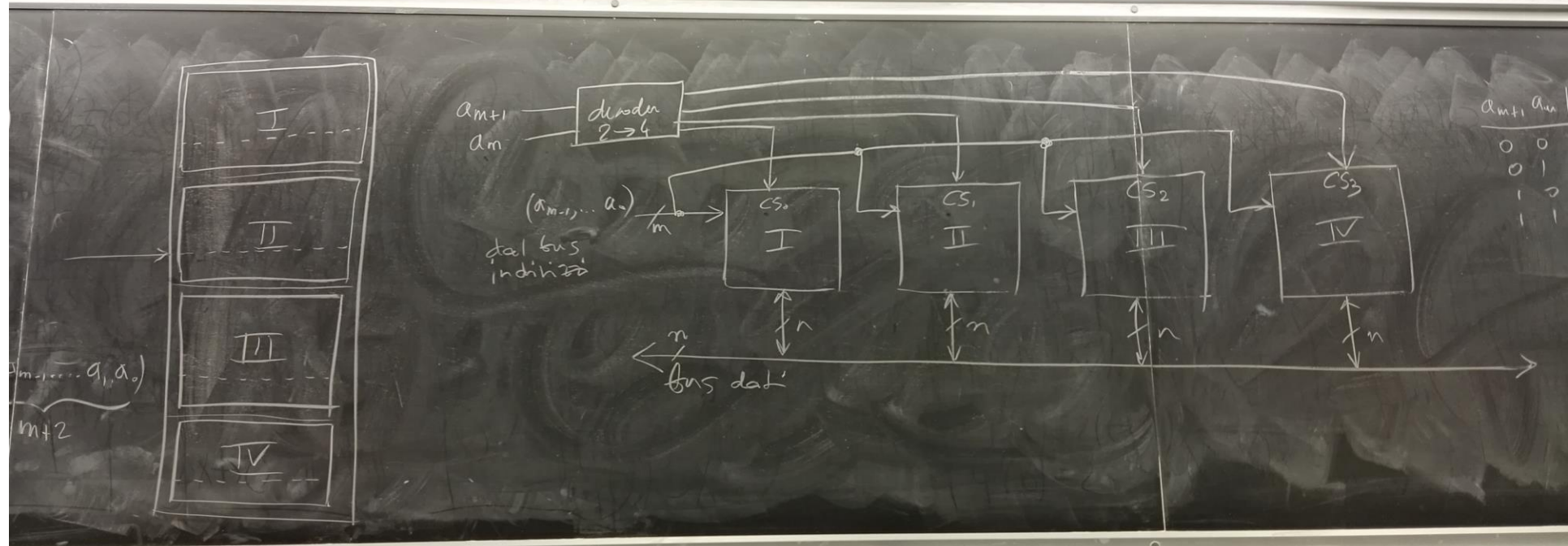
indirizzi, dati, controlli

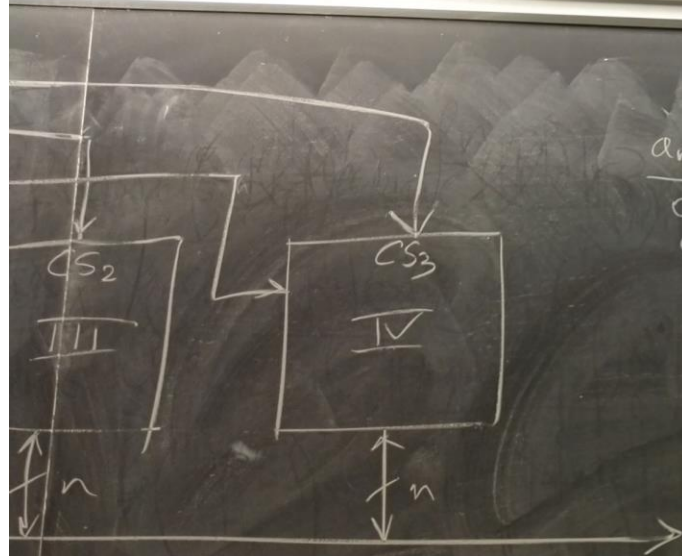
decoder binario

Leggi di De Morgan

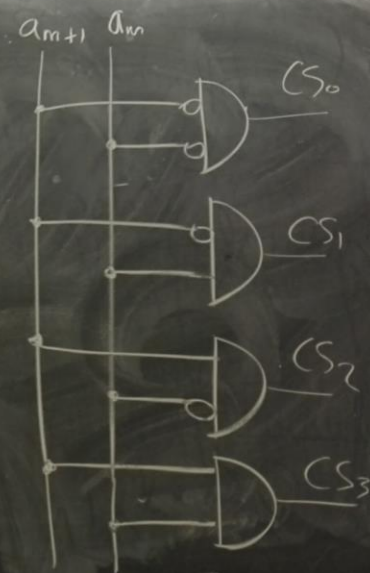
$(a_{m+1}, a_m, a_{m-1}, \dots, a_1, a_0)$
 $m+2$

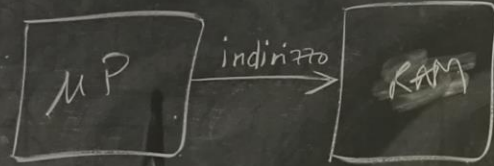






a_{m+1}	a_m	CS_0	CS_1	CS_2	CS_3
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1





a_{m+1}	a_m	a_{m-1}	...	a_2	a_1	a_0
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a_{m+1}	a_m	a_{m-1}	a_{m-2}	...	a_2	a_1	a_0
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chip nel banco

parola dati nel chip

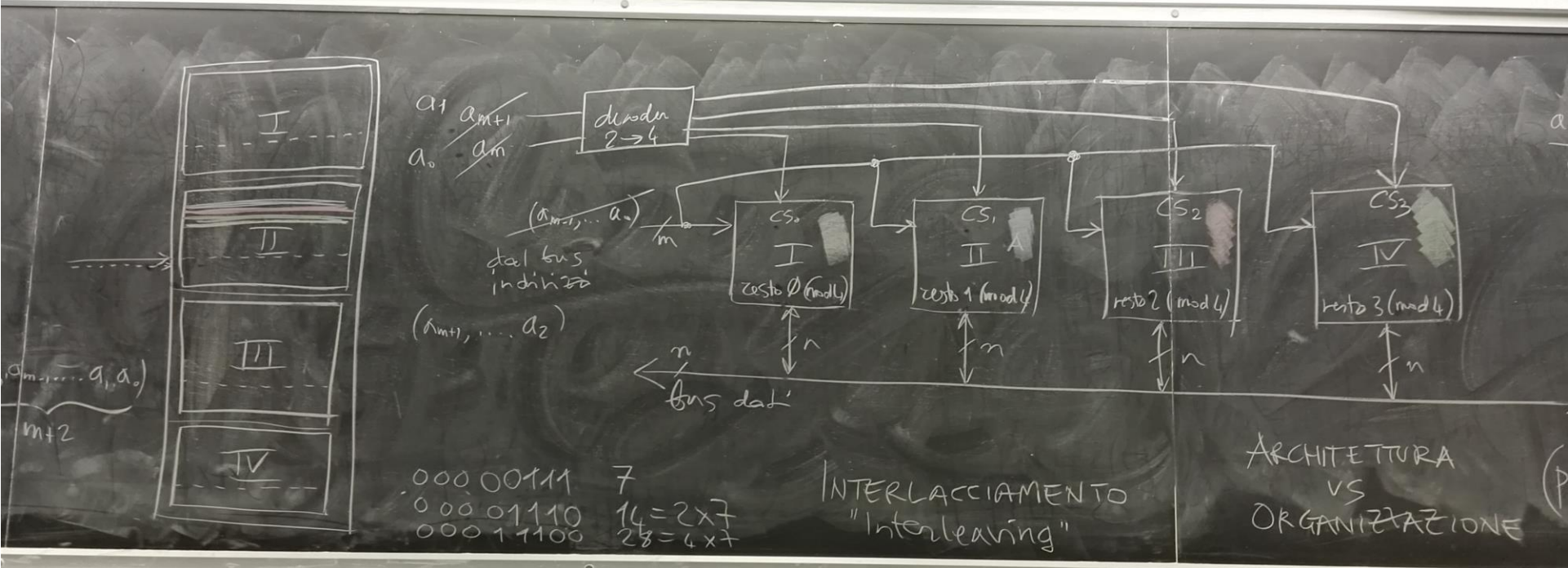
$\frac{\text{chip}}{\text{banco}} \times \frac{\text{parola}}{\text{chip}}$

$\boxed{xxx \dots xx} \underline{00}$

parola dati nel banco

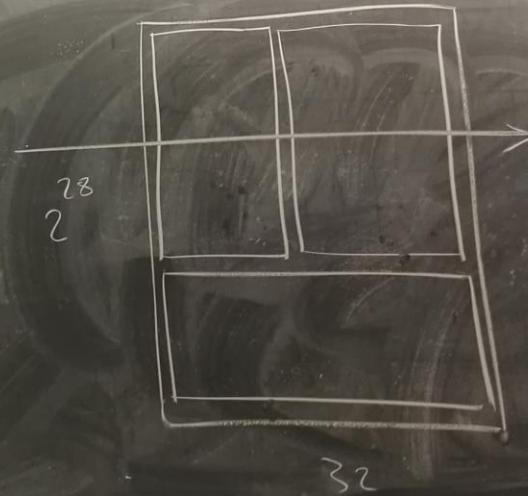
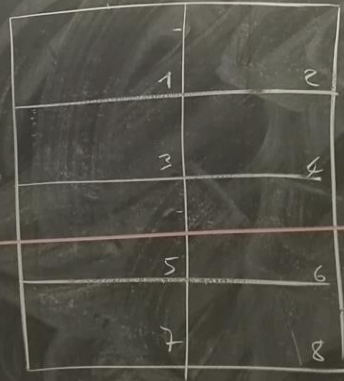
$(a_{m+1}, a_m, a_{m-1}, \dots, a_0)$
 $m+2$

da De Morgan



compito 30/3/2009

progetto di un
banco di memoria
con chip eterogenei



$$S = 1GB$$

$$n = 32$$

$$S = 2^m \times n \text{ (bit)}$$

$$2^{30} = 2^m \times 4 \text{ (byte)}$$

$$\downarrow$$

$$2^2$$

$$\Rightarrow m = 28$$

ORGANIZZAZIONE

tipo	dimensione (MB)	# bit indirizzo	# bit dato	partiz
A	256	28	8	1
B	256	27		1
C		26	16	1
D	128		8	1
E	64	26		

