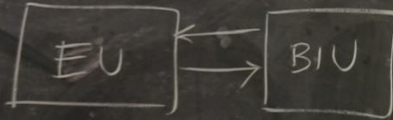


8086

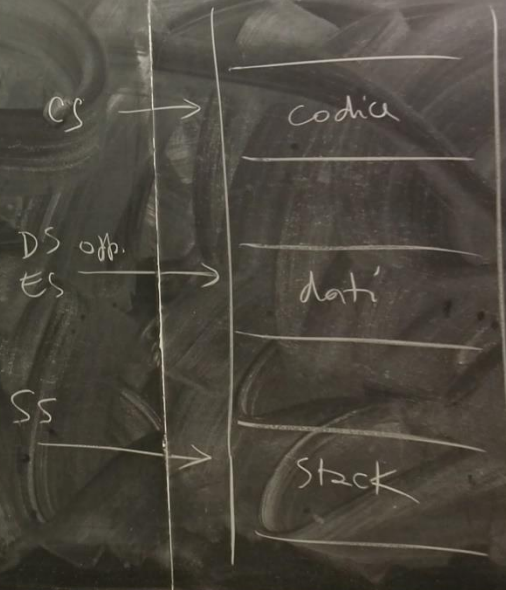
1979

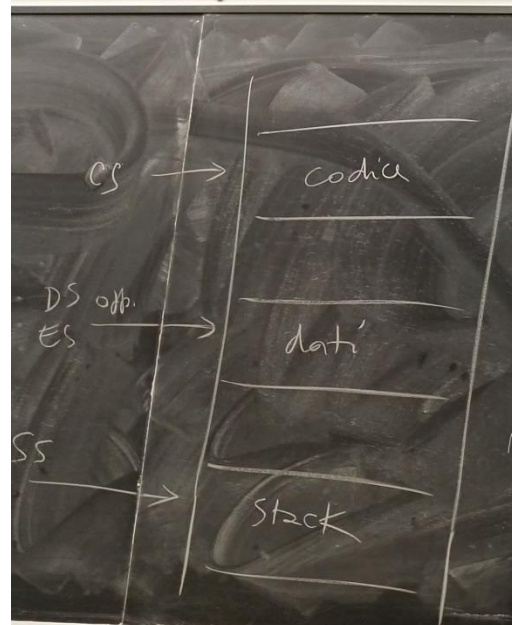
dat': 16 bit
indirizz: 20 bit



memoria
segmentata

CS, DS, ES, SS
registri di
segmento





GPR

8	8
AH	AL
BH	BL
CH	CL
DH	DL

16

AX ← accumulatore
 BX ← indirizzamenti
 CX ← counter
 DX ← dati

NON PIÙ DI DUE OPERANDI

ADD AX, BX ; AX ← AX + BX

AL MAX 1 OPERANDO DI MEMORIA

(No) ADD VAR1, VAR2

LOOP PIPPO ⇔

DEC CX
 JNZ PIPPO

←
 MOV AX, VAR1
 MOV DL, [BX]
 MOV BX, OFFSET
 * & ←
 CS:IP =
 160

AX ← accumulatore
 BX ← indirizzamento
 CX ← counter
 DX ← dati

[AX]

LOOP PIPPO ⇔ DEC CX
 JNZ PIPPO
 X ← AX + BX
 DI MEMORIA

←
 MOV AX, VAR
 MOV DL, [BX]
 MOV BX, OFFSET VAR

*

&

EAX
 CS:IP = Program Counter

16Bit | IP (Instruction Pointer)

pointatori

SI
DI
SP
BP

16 bit

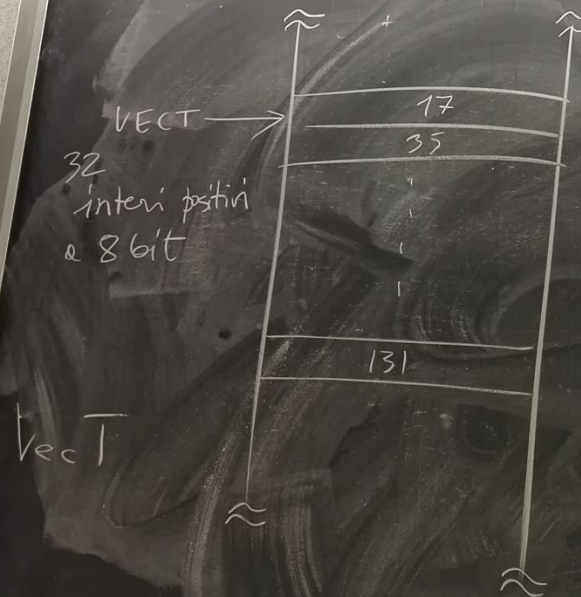
} lavoro con la memoria RAM
 } lavoro nella stack

PUSH,
 POP

PSW = (flags)

CF	SF	OF	ZF	1
----	----	----	----	---





8086
1979

esercizio
Assembly 8086 =

Sommare gli elementi di
vect e porre il risultato
nella variabile SOMMA

←
area
dati

DL, [BX]

AX, DX

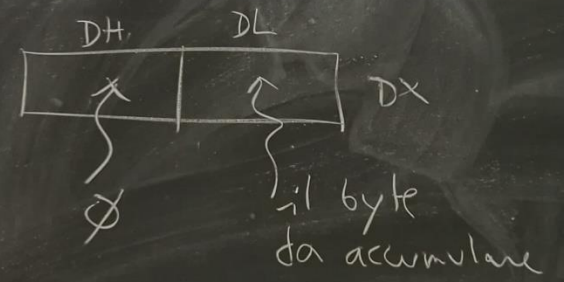
BX

CX

CICLO

SOMMA, AX

←
ADD AX, DX accumulazione



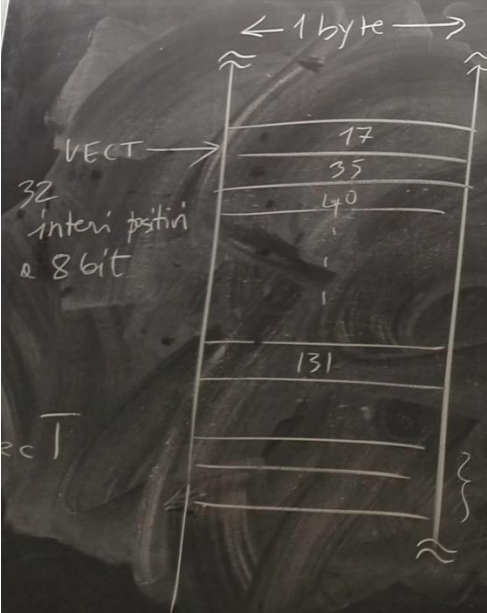
n equ 32
vect db n dup(0) ; in alt. : ?, 'r'
Somma dw ?
*istanza di
(riempimento del vettore...)*

ACCUMULA:
XOR AX, AX
XOR DH, DH
MOV CX, n
MOV BX, offset VECT

→ CICLO:

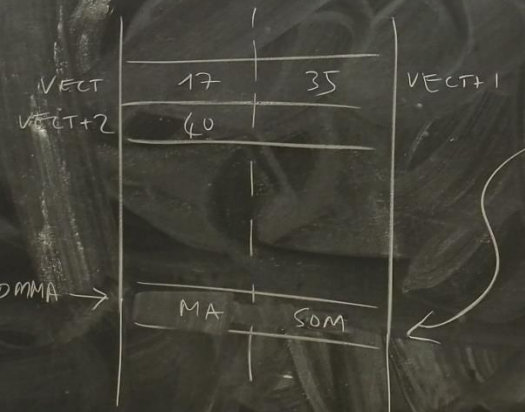
MOV DL, [BX]
ADD AX, DX
INC BX
DEC CX
JNZ CICLO

SALVA: MOV SOMMA, AX



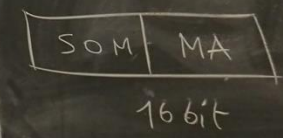
8086
1979

1 word =
← 2 byte →

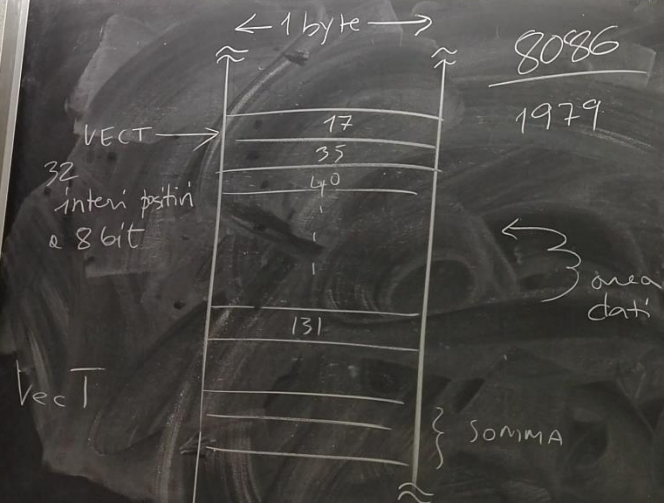


n equ 32
vect db n dup(0)
somma dw ?
istogrammi
(Z'empimento)

ACCUMULA:
"Little endian"
"Big Endian"



XOR AX, AX
XOR DH, DH
MOV CX, n
MOV BX, c



int accumula (n, vect)

CHIAMANTE:

MOV AX, n
PUSH AX
MOV BX, offset VECT
PUSH BX

CALL ACCUMULA

POP DX
MOV SOMMA, DX
ADD SP, 2

CS:IP
(PC)

CALL ACCU
POP SOMMA

1. salva il PC di ritorno
2. modifica il PC
con l'indirizzo
della routine
ACCUMULA

(n, vect)

n

offset VECT

CALL ACCUMULA

MA, DX
Z

CS:IP
(PC)

CALL ACCUMULA
POP EMMMA

chiamante

ACCUMULA:

PUSH AX

RET

ACCUMULA:

PUSH AX
PUSH BX
PUSH CX
PUSH DX
PUSH BP

MOV BP, SP

ADD BP, 12

MOV CX, [BP+2]

MOV BX, [BP]

XOR AX, AX
XOR DH, DH
Ciclo: (...)

POP BP

POP DX

POP CX

POP BX

POP AX

RET

SP = BP + 4

push

BP^{new} = BP + 12
BP^{old}

SP



ACCUMULA:

PUSH AX
 PUSH BX
 PUSH CX
 PUSH DX
 PUSH BP

MOV BP, SP
 ADD BP, 12
 MOV CX, [BP+2]
 MOV BX, [BP]

XOR AX, AX
 XOR DH, DH
 ciclo: (...)

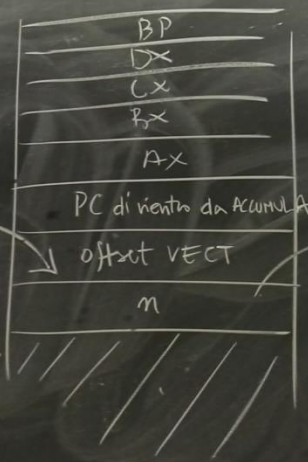
POP BP
 POP DX
 POP CX
 POP BX
 POP AX
 RET

SP = BP^{old}

push
 BP^{new} = BP^{old} + 12

SP

← 2 bytes →



stack
 pop

Conversione
 8086
 (e ARM) =
 stack
 "pieno"
 e
 "discendente"

push AX { SUB SP, 2
 MOV [SP], AX
 pop DX { MOV DX, [SP]
 ADD SP, 2

